Alpha Delay Switches:

After Step 1:

* Location 12 – Bits 1, 2, 3:
  + No phase words that will set the 3 LSB to 1
* Location 28 – Bit 1:
  + No phase words that will set the LSB to 1
* Location 35 – Bit 4:
  + “0011000”: -245
  + “0001100”: 252
* Location 44 – Bits 1, 2:
  + No phase words that will set the 2 LSB to 1
* Location 60 – Bit 1:
  + No phase words that will set the LSB to 1
* Location 75 – Bit 7:
  + “1100000”: -253
* Location 77 – Bit 7:
  + “1100000”: 244
* Location 92 – Bits 2 to 7:
  + “0000011”: 62
  + “0000111”: 60
  + “0001111”: 56
  + “0011111”: 48
  + “0111111”: 32
  + “1111111”: 0
* Location 108 – Bits 1 to 7:
  + No phase words that will set the LSB to 0
  + “0000011”: 31
  + “0000111”: 30
  + “0001111”: 28
  + “0011111”: 24
  + “0111111”: 48
  + “1111111”: 32
* Location 117 – Bit 6:
  + “1100000”: 6
* Location 124 – Bits 2 to 7:
  + “0000011”: 42
  + “0000111”: 20
  + “0001111”: 40
  + “0011111”: 80
  + “0111111”: 32
  + “1111111”: 64
* Location 140 – Bits 1 to 7:
  + No phase words that will set the 2 LSB to 0
  + “0000111”: 15
  + “0001111”: 14
  + “0011111”: 12
  + “0111111”: 8
  + “1111111”: 16